

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	("6052171").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/08 15:07
L2	1222	(spark (static adj electric\$4)) and (dummy)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/08 15:09
L3	207	(spark (static adj electric\$4)) same (dummy)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/08 15:10
L4	88	3 and (driving driver (circuit adj board) PCB)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/08 15:11
L5	172	"349"/\$.ccls. and (dummy near2 (wir\$4 signal\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/08 15:18
L6	162	5 and (driv\$4 and display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/08 15:35
L7	265	"345"/\$.ccls. and (dummy near2 (wir\$4 signal\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/08 15:34
L8	446	"345"/\$.ccls. and (dummy near2 (wir\$4 signal\$4 line\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/08 15:34
L9	249	(driv\$4).ab. and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/08 15:55

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L10	37	((driv\$4 near2 circuit) AND semiconductor AND (data with (transfer\$4 connect\$4) with (line\$1 signal\$4 line\$1)) AND dummy).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/08 16:03
L11	77	((driv\$4 near2 circuit) AND semiconductor AND (electrode voltage) AND ((line\$1 signal\$4 line\$1) with dummy)).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/08 16:04